

Instrument Firmware Release Notes

Models

DG1022, DG1022A

Version Information

Released version: 00.03.00.00.01

Release Date: 12/11/2014

Compatibility

Compatible with earlier versions of the hardware versions > 03.

If hardware is < 03, you <u>cannot</u> upgrade using this firmware and any attempt could permanently damage the instrument. Any attempt to do so will void the warranty. Contact your local Rigol office if you have hardware < 03.

If the current boot version is < 03, you will need to update the bootloader first.



DG1022A/U: DSP Version <= **00.01.05.00.00**

DG1022: DSP Version <= **00.02.12.00.00**

DG1022X(DSP)Update(Bootloader)

Boot Version 03

DG1022X (DSP)update(Normal)

DSP version:00.03.00.00.01



Change Log History

Key:

E – Enhancements. Changes that add functionality

M – Modifications. Changes to improve performance on existing features

C – Cancellation. Removal of function or capability

*Note: Engineering change log is translated from engineering documentation

| Version: 00.03.00.00.01 Date: 2014-12-11 | | | |
|--|--|-------------------------------|---|
| 1 | Add the function that support to lock keyboard through the special file in | | Е |
| | USB disk, which also support to process the SCPI command that is in USB | | |
| | disk. | | |
| 2 | Fixed the bug which can result in the device's screen to be white and can not work anymore. | | М |
| 3 | Fixed the bug which can result in the device's screen to be black when calibrating manually. | | М |
| 4 | Cooperate with the FPGA version 00.13 to set the idle level to zero when DDS is off. | | М |
| 5 | Add the SCPI command that can be used to query the state of clock source. | | Е |
| 6 | Modify the built-in cardiac waveform to fixed the lack of amplitude. | e problem which result in the | М |
| 7 | Combine the version of DG1022,DG1022A and be used to update all these three models. | DG1022U, this version can | Е |



| Version: 00.01.04.00.01 | | | |
|-------------------------|---|--------------------------|---|
| 1 | Fixed the bug: the output signal of two piece of dg1022 can not be synchronized when they work in external clock mode. | | M |
| 2 | Fixed the bug: Set the dg1022 to work in external clock mode, the dg1022 can display "Overload protect, output off!" while reboot the dg1022 with last setting, and set ch1 on. | | М |
| 3 | Fixed the bug: display is error when setting the dg1022 to display ch2 in single channel picture mode, and the current waveform of ch2 is arbitrary waveform. | | M |
| | Fixed the bug: the output waveform is wrong when setting dg1022 worked in am mode, and the modulation shape is choose as volatile arbitrary waveform. | | М |
| | This version compatible the old fpga version 00.09 and the newest fpga version 00.11. | | Е |
| | Fixed the bug: the output will be stopped when the dg1022 have been working a long time(usually working 2 days continuously). | | M |
| | Enhance the stability of DG1022: modify the disable interrupt. | code about enable or | Е |
| | Integrate the code of dg1022 to dg1022A ar | nd DG1022U, now there is | M |
| | just the calibration value is different as dg10 | 022 in this version. | |



End of Change Log History

For more information in North America, please go to **rigolna.com**, contact us directly at **techsupport@rigol.com** or call us toll free at **877-4-RIGOL-1**.

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