RIGOL

Declassification Guide

DG1000Z Series Function/Arbitrary Waveform Generator

Apr. 2020
RIGOL TECHNOLOGIES CO., LTD.

DG1000Z Series

DG1000Z series oscilloscope consists of DG1062Z, DG1032Z, and DG1022Z.

Instrument Memory

This section contains information on the types of memory available in your instrument. It explains the size of memory, how it is used, its location, volatility and the clearing procedure.

Instrument memory:

Memory	Writable	Data	Purpose/	Data input	Location in	Sanitization
type and	during	retained	contents	method	instrument	procedure
size	normal	when			and remarks	
	operation	powered off				
FRAM	YES	YES	System	Operating	Main board in	Default all
			setting\	system	CPU area	settings
			License Data			
(Nand Flash)	YES	YES	System	Firmware	Main board in	Cycle power
1Gb			firmware\	upgrade\	CPU area	
			FPGA	Calibration\		
			firmware\	System		
			Calibration			
			data\			
			User setting			
(SPI Flash)	NO	YES	BOOTLOADER	Factory	Main board in	No user data
4Mb				Manufacturer	CPU area	is
						stored
Data Memory	YES	NO	System	System	Main board in	Cycle power
(DDR2)			Application &		CPU area	
256Mb			Kernal RAM			
Data Memory	YES	NO	ARB-SIGNAL	SYSTEM	Main board in	Cycle power
(DDR2)			GENERATED		CPU area	
256Mb			BY USER			
			/DDS			
			REFERANCE			
			SINAL			

Position of Instrument Memory on Main Board

