RIGOL

Declassification Guide

DL3000 Programmable DC Electronic Load

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RIGOL TECHNOLOGIES CO., LTD.

DL3000 Series

DL3000 series consists of DL3021, DL3021A, DL3031, and DL3031A.

Instrument Memory

This section contains information on the types of memory available in your instrument. It explains the size of memory, how it is used, its location, volatility and the clearing procedure.

Instrument memory:

Memory	Writable	Data	Purpose/	Data input	Location in	Sanitization
type and	during	retained	contents	method	instrument	procedure
size	normal	when			and remarks	
	operation	powered off				
FRAM	YES	YES	System	Operating	Main board in	Default all
			setting\	system\	CPU area	settings
			License Data\	Calibration\		
			Calibration			
			data\			
(Nand Flash)	YES	YES	System	Firmware	Main board in	Cycle power
128MB			firmware\	upgrade\	CPU area	
			FPGA	System		
			firmware\			
			User setting			
(SPI Flash)	NO	YES	BOOTLOADER	Factory	Main board in	No user data
128KB				Manufacturer	CPU area	is
						stored
Data Memory	YES	NO	System	System	Main board in	Cycle power
(DDR2)			Application &		CPU area	
64Mb			Kernal RAM			

Position of Instrument Memory on Main Board

