RIGOL

Declassification Guide

DSG800/DSG800A
RF SIGNAL GENERATORS

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RIGOL TECHNOLOGIES, INC.

DSG800/DSG800A Series

DSG800 series RF signal generators consists of DSG830 and DSG815.
DSG800A series RF signal generators consists of DSG836A,DSG836,DSG830,DSG821A,DSG821 and DSG815.

Instrument Memory

This section contains information on the types of memory available in your instrument. It explains the size of memory, how it is used, its location, volatility and the clearing procedure.

Instrument memory:

Memory type and size	Writable during normal operation	Data retained when powered off	Purpose/ contents	Data input method	Location in instrument and remarks	Sanitization procedure
FRAM 16kb	Yes	Yes	System setting\ License Data	Operating system	Digital board in CPU area	Default all settings
(Nand Flash) 1Gb	Yes	Yes	System firmware\ FPGA firmware\ Calibration data \ User setting	Firmware upgrade \ Calibration\ System	Digital board in CPU area	Cycle power
(Spi Flash) 4Mb	No	Yes	Boot loader	BOOT	Digital board in CPU area	No user data is stored
Data Memory (DDR2) 512Mb	Yes	No	FPGA Code\ caches	System	Digital board in CPU area	Cycle power

Position of Instrument Memory on Digital Board